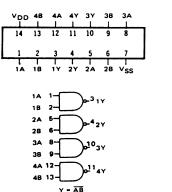
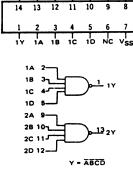
SCL4011B

QUAD TWO-INPUT NAND GATE

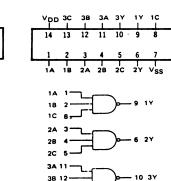




SCL4012B

DUAL FOUR INPUT NAND GATE

VDD 2Y 2D 2C 2B 2A NC



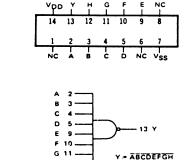
SCL4023B

Y - ABC

TRIPLE THREE INPUT NAND GATE



EIGHT INPUT NAND GATE



## STATIC CHARACTERISTICS: ( $V_{SS} = 0 V$ )

PARAMETER	CONDITIONS	V <sub>DD</sub> (Vdc)	T <sub>LC</sub> MIN	₩* MAX	MIN	+ 25°C TYP	MAX	T <sub>HI</sub> MIN	GH** MAX	UNIT
QUIESCENT DEVICE	$V_{IN} = V_{SS} \text{ OR } V_{DD}$	5		0.05		0.0005	0.05		1.5	
CURRENT IDD		10		0.1		0.001	0.1		3.0	µAdc
		15		0.2		0.002	0.2		6.0	

Note:  $T_{LOW} = -55^{\circ}C$  for C / H devices, -40°C for E / S devices,  $*T_{HIGH} = +125^{\circ}C$  for C / H devices, +85°C for E / S devices.

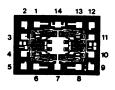
## **DYNAMIC CHARACTERISTICS:** ( $CL = 50pF, TA = 25^{\circ}C$ )

PARAMETER	V <sub>DD</sub> Vdc	MINIMUM	TYPICAL	ΜΑΧΪ́ΜυΜ	UNIT
PROPAGATION DELAY	5		125	250	
TIME t <sub>PLH</sub> , t <sub>PHL</sub>	10		60	120	ns
	15		45	90	
OUTPUT TRANSISTION	5		100	200	
TIME t <sub>TLH</sub> , t <sub>THL</sub>	10		50	100	ns
	15		40	80	

## **DIE DRAWINGS**

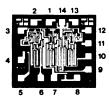
SCL4011B 53 x 37 mils

SCL4012B 43 x 43 mils

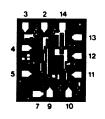




SCL4023B 53 x 46 mils



SCL4068B 48 x 52 mils



Note: Refer to "SCL4000B SERIES FAMILY SPECIFICATIONS" for remaining Dynamic & Static Characteristics, and, for recommended and maximum operating conditions.

This datasheet has been downloaded from http://www.digchip.com at this page